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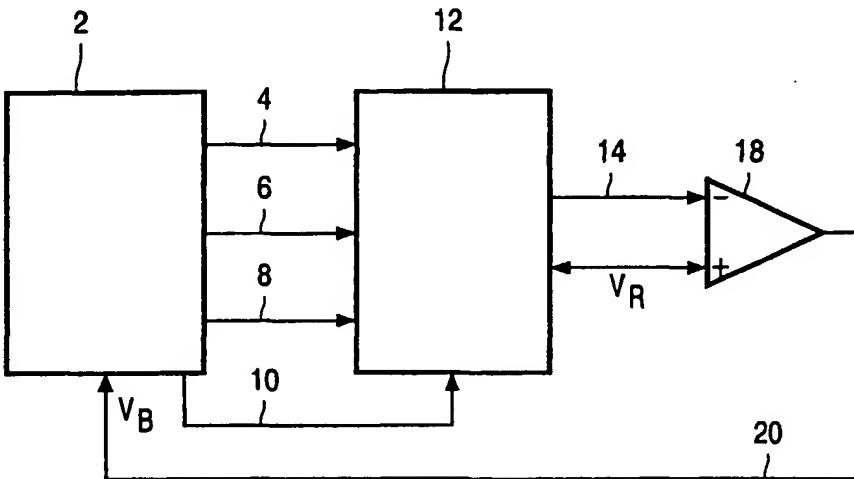
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(54) Title: CIRCUIT AND METHOD FOR CONTROLLING THE THRESHOLD VOLTAGE OF TRANSISTORS



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(57) Abstract: A control unit controlling a threshold voltage of a circuit unit (2) having a plurality of transistor devices, comprising a reference circuit; a measuring unit (12) measuring a threshold voltage of at least one sensing transistor of the circuit unit (2) and measuring a threshold voltage of at least one reference transistor of the reference circuit; a differential voltage generator (18) generating a differential voltage from outputs of the measuring unit (12) and a bulk connection of the transistor devices in the circuit unit (2) to which the differential voltage is fed as a biasing voltage (V_B).



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INTERNATIONAL SEARCH REPORT

International Application No

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A. CLASSIFICATION OF SUBJECT MATTER

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 397 934 A (FARRENKOPF DOUG R ET AL) 14 March 1995 (1995-03-14) column 10, line 49 -column 11, line 30; figure 4 -----	1-17
A	US 5 017 811 A (WORLEY EUGENE R) 21 May 1991 (1991-05-21) column 3, line 46 -column 4, line 17; figure 4 -----	1-17
A	US 4 791 318 A (LEFTON SCOTT ET AL) 13 December 1988 (1988-12-13) the whole document -----	1-17

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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Patent document cited in search report	Publication date		Patent family member(s)		Publication date
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US 4791318	A	13-12-1988	NONE		